

Symposium Program

3rd December (Monday)

Venue: ENEOS Hall

Komaba Research Campus, the University of Tokyo

Registration (9:00-9:50)

Welcome Address (9:50-10:00)

S. Tahara (*PETRA*)

Session A: Opening (10:00-11:55)

10:00 **A-1 (Keynote)**

Advances in Photonic and Electronic Convergence System Technology

Y. Arakawa (*The University of Tokyo*)

10:30 **A-2 (Plenary)**

Electronic-Photonic Integration Platforms: Datacom, RF and Sensing

L. C. Kimerling (*Massachusetts Institute of Technology*)

11:15 **A-3 (Invited)**

Photonic Integration of Ultra-High-Q Optical Resonators for Next-Generation Clocks and Hertz-Absolute-Accuracy Optical Frequency Synthesizers

K. Vahala (*California Institute of Technology*)

11:55-13:20 Lunch break

Session B: Silicon Nanophotonics Devices & Systems I (13:20-15:20)

13:20 **B-1 (Invited)**

Silicon Photonics Packaging for Optical Interconnects and Integrated Sensors

C. Xiong (*IBM Research*)

14:00 **B-2 (Invited)**

Silicon Photonic Switching Toward Future Cloud Networking

S. Namiki (*AIST*)

14:40 **B-3 (Invited)**

Femtojoule 3D Nanophotonic-Nanoelectronic Integrated Circuits for Computing Beyond Moore's Law

S. J. B. Yoo (*University of California, Davis*)

15:20-15:40 Break

Session C: Silicon Nanophotonics Devices & Systems II (15:40-18:05)

15:40 **C-1 (Invited)**

Beyond 110Gbit/s OOK and PAM-4 Signaling Using a Single Drive Hybrid Silicon and Polymer Modulator

S. Yokoyama (*Kyusyu University*)

16:20 **C-2**

Toward Optical Interposer for 10-Tbps Interconnection

T. Nakamura (*PETRA*)

16:40 **C-3**

High-Density Silicon Photonics Technologies toward Large-Capacity Optical Interconnection

Y. Tanaka (*PETRA*)

17:00 **C-4**

Optical and Electrical Hybrid Interposer using Single Mode Polymer Optical Waveguides

T. Amano (*AIST*)

17:20 **C-5**

Optical Hub for Rack-Scale Parallel-Processing Systems

Y. Urino (*PETRA*)

17:40 **C-6**

Efficient Optical Phase Modulator Based on Si Hybrid MOS Capacitor

M. Takenaka (*The University of Tokyo*)

Banquet (18:30-20:00): ape cucina naturale

4th December (Tuesday)

Venue: ENEOS Hall

Komaba Research Campus, the University of Tokyo

Session D: Silicon Nanophotonics Devices & Systems III (9:30-11:30)

9:30 **D-1 (Invited)**

Co-Packaging of Ethernet Switches & Optical I/Os Enabled by Silicon Photonics

P. De Dobbelaere (*Luxtera*)

10:00 **D-2 (Invited)**

Deci-Dime Scale Optical Transceiver "Optical I/O core" Based on Si Photonics Technology

H. Fukuda (*AIO CORE*)

10:30 **D-3 (Invited)**

High-Density Si Photonics for In-Package Optics

A. Zilkie (*Rockley Photonics*)

11:00 **Panel Discussion**

Moderator: S. Tahara (*PETRA*)

Panelists: P. De Dobbelaere, H. Fukuda, and A. Zilkie

Poster Session (11:30-14:00)

11:30 **Poster preview**

12:30 **Poster presentation with Lunch**

12:30-14:00 Lunch break

Session E: Silicon Nanophotonics Devices & Systems IV (14:00-15:55)

14:00 **E-1 (Invited)**

TE Mode Optical Isolator for Silicon Photonics

T. Mizumoto (*Tokyo Institute of Technology*)

14:40 **E-2 (Invited)**

Reliable, Feedback Insensitive p-Modulation Doped Quantum Dot Lasers Epitaxially Grown on CMOS Compatible Silicon Substrates

J. Norman (*University of California, Santa Barbara*)

15:20 **E-3**

Light Propagation in Semiconductor Valley Photonic Crystal Slab

S. Iwamoto (*The University of Tokyo*)

Closing Address (15:45-15:55)

M. Mori (*AIST*)

Poster Session (Tuesday)

P-01

Si Waveguide Polarization Beam Splitter using Reversed $\Delta\beta$ Directional Coupler

H. Okayama^{1,2}, Y. Onawa^{1,2}, D. Shimura^{1,2}, H. Yaegashi^{1,2}, and H. Sasaki^{1,2}

(1 OKI Electric Industry Co. Ltd., 2 PETRA)

P-02

The Novel Frequency Modulator Design and Synthesis based on BJT Transistor

K. Tripetch¹

(1 Rajamangala University of Technology Suvarnabhumi)

P-03

High-Efficiency and High-Resolution Si Photonic Crystal Slow-Light Beam Steering Device

H. Ito¹, Y. Kusunoki¹, D. Akiyama¹, H. Abe¹, G. Takeuchi¹, and T. Baba¹

(1 Yokohama Nat'l University)

P-04

High-Speed Operation of Meander Line Electrode Photonic Crystal Optical Modulator

Y. Hinakura¹, H. Arai¹, and T. Baba¹

(1 Yokohama Nat'l University)

P-05

Uniaxial Crystalline Yttrium Iron Garnet on Amorphous SiO₂ by Layer-by-Layer Pulsed LASER Deposition

G. Nakamura¹, and H. Isshiki¹

(1 The University of Electro-Communications)

P-06

Fabrication of Vertical DBR Cavity with Er Silicate Active Region using Radical Assisted Sputtering

G. D. Fuentes¹, T. Kasumi¹, Y. Tanaka^{1,2}, and H. Isshiki¹

(1 The University of Electro-Communications, 2 Shincron Co. Ltd.)

P-07

Investigation of a Germanium-on-Insulator Band-Stop Filter

C. P. Ho^{1,2}, Z. Zhao¹, Q. Li¹, S. Takagi¹, and M. Takenaka¹

(1 Univ. Tokyo, 2 JSPS Research Fellow)

P-08

Fabrication of High-Q Ring Resonator Using Low Loss GeOI Wafer

Z. Zhao¹, C. P. Ho¹, S. Takagi¹, and M. Takenaka¹

(1 Univ. Tokyo)

P-09

Demonstration of Si Racetrack Resonator Based on III-V/Si Hybrid MOS Phase Shifter

Q. Li¹, C. P. Ho², S. Takagi¹, and M. Takenaka¹

(1 Univ. Tokyo, 2 JSPS Research Fellow)

P-10

Estimation of Performance and Measurement of Beam Steering of Si Photonics FMCW LiDAR

H. Abe¹, H. Ito¹, and T. Baba¹

(1 Yokohama Nat'l University)

P-11

Packaging of Hybrid-Integrated Light Sources Mounted on Silicon Platform

M. Nishizawa¹, S. -H. Jeong¹, and Y. Tanaka^{1,2,3}

(1 PETRA, 2 Fujitsu Ltd., 3 Fujitsu Laboratories Ltd.)

P-12

Si (De)Multiplexers with Automated Fabrication-Error Correction

T. Akiyama^{1,2,3}, T. Aoki¹, T. Mori², T. Simoyama^{1,2,3}, S. -H. Jeong¹, N. Hatori¹, S. Sekiguchi¹, Y. Sobu^{1,2,3}, S. Tanaka^{1,2,3}, M. Nishizawa¹, A. Sugama², A. Hayakawa^{1,2,3}, and Y. Tanaka^{1,2,3}

(1 PETRA, 2 Fujitsu Laboratories Ltd., 3 Fujitsu Ltd.)

P-13

Cobalt Ferrite with Large Magneto-Optical Effect for Silicon Photonics

M. A. Serrano-Nunez¹, Y. Shoji^{1,2}, and T. Mizumoto^{1,2}

(1 Dept. of EEE, Tokyo Inst. Tech., 2 FIRST, Tokyo Inst. Tech.)

P-14

High-Speed Ge/Si Electro-Absorption Optical Modulator for High-Bandwidth Optical Interconnect

J. Fujikata¹, M. Noguchi¹, K. Kawashita², S. Takahashi¹, M. Nishimura^{2,3}, H. Ono¹, D. Shimura¹, H. Takahashi¹, H. Yaegashi¹, Y. Ishikawa², and T. Nakamura¹

(1 PETRA, 2 Toyohashi Univ. Tech, 3 Tokyo Univ.)

P-15

Numerical Study on Modulation Efficiency Enhancement of SIS Optical Phase Shifter Using Negative Capacitance Effect

J. -H. Han^{1,3}, K. Bidenko^{1,2,3}, J. Song^{1,2}, S. -H. Kim^{1,2}

(1 KIST, 2 KIST school UST, 3 These authors contributed equally to this work.)

P-16

Low-Loss Silicon Waveguide Optical Isolator Fabricated by Direct Bonding

R. Tian¹, Y. Shoji¹, and T. Mizumoto¹

(1 Tokyo Inst. Tech.)

P-17

Energy Cost Study of Membrane Distributed-Reflector (DR) Lasers for High-Speed Modulation

N. Nakamura¹, T. Yoshida², W. Fang¹, T. Amemiya^{1,2}, N. Nishiyama^{1,2}, and S. Arai^{1,2}

(1 Dept. of EEE, Tokyo Inst. Tech., 2 IIR Tokyo Inst. Tech.)

P-18

Magneto-Optical Photonic Crystal Waveguides

K. B. Walid¹, K. Ogawa¹, Y. Shoji¹, and T. Mizumoto¹

(1 Tokyo Inst. Tech.)

P-19

Reception Antenna Characteristics of Si Photonics Crystal Optical Beam Steering Device

Y. Furukado¹, H. Ito¹, Y. Kusunoki¹, D. Akiyama¹, H. Abe¹, and T. Baba¹

(1 Yokohama Nat'l University)

P-20

Study of Microcrystalline Silicon Optical Modulator with Multi-layered Structure

K. Matsumoto¹, Y. Kondo¹, R. Takei², Y. Shoji¹, T. Mizumoto¹, and T. Kamei²

(1 Tokyo Inst. Tech., 2 AIST)

P-21

Control of Light Emission Characteristics from a Serial Array of Si Photonic Crystal Beam Steering Devices

R. Tetsuya¹, H. Abe¹, and T. Baba¹

(1 Yokohama Nat'l University)

P-22

Design of a High-Q Coupled Nanocavity System for Dynamic Photon Manipulations

M. Nakadai^{1,2}, T. Asano¹, and S. Noda¹

(1 Kyoto Univ., 2 JSPS Research Fellow)

P-23

Optimization of Doubly-Periodic Photonic Crystal Waveguide for Improving the Transmission and Reception Efficiency in Si Photonics Slow-Light Antenna

G. Takeuchi¹, H. Abe¹, and T. Baba¹

(1 Yokohama Nat'l Univ.)

P-24

Single Sideband Modulation Using Si Photonic Crystal Optical Modulator

M. Kamata¹, Y. Hinakura¹, and T. Baba¹

(1 Yokohama Nat'l Univ.)

P-25

1200nm-Band InAs Quantum Dot Intermixing for Monolithic Photonic Integrated Circuits in Data Centers

K. Utaka¹, T. Shirai¹, Y. Hiraishi¹, Y. Matsushima¹, H. Ishikawa¹, Y. Arakawa², and J. -K. Kwoen²

(1 Waseda Univ., 2 Tokyo Univ.)

P-26

Demonstration of a Topological Photonic Crystal Nanocavity Laser with Quantum Dot Gain

Y. Ota¹, R. Katsumi², K. Watanabe¹, S. Iwamoto^{1,2} and Y. Arakawa¹

(1 NanoQuine, Univ. Tokyo, 2 IIS, Univ. Tokyo)

P-27

Demonstration of a Quantum Dot Laser on Silicon using Transfer Printing

B. -Y. Jang¹, Y. Ota¹, R. Katsumi², J. -K. Kwoen¹, N. Morais¹, and Y. Arakawa¹

(1 NanoQuine, Univ. Tokyo, 2 IIS, Tokyo Univ.)

P-28

Over 100 °C CW Operation of InAs/GaAs Quantum-Dot Lasers on Si (001) Substrate

J. -K. Kwoen¹, B. -Y. Jang¹, K. Watanabe¹ and Y. Arakawa¹

(1 NanoQuine, Univ. Tokyo)

P-29

On-Chip Excitation of Single Quantum Dots using a Silicon Waveguide

A. Osada¹, Y. Ota¹, R. Katsumi², T. Yamaguchi², M. Kakuda¹, S. Iwamoto^{1,2}, and Y. Arakawa¹

(1 NanoQuine, Univ. Tokyo, 2 IIS, Tokyo Univ.)

P-30

Growth of InAs/GaAs Bilayer Quantum Dots for Long-Wavelength Emission

W. Zhan¹, J. -K. Kwoen¹, K. Watanabe¹, S. Iwamoto^{1,2}, and Y. Arakawa¹

(1 NanoQuine, Univ. Tokyo, 2 IIS, Tokyo Univ.)

P-31

MBE Growth of Stacked InAs/GaAs Quantum Dots with Thin (25.5 nm) Barrier Layer

M. Kakuda¹, J. -K. Kwoen¹, K. Watanabe¹, and Y. Arakawa¹

(1 NanoQuine, Univ. Tokyo)

P-32

Observation of Light Propagation through Sharp Bends in a Slab-type Valley Photonic Crystal Waveguide

T. Yamaguchi¹, Y. Ota², R. Katsumi¹, S. Ishida¹, A. Osada², Y. Arakawa², and S. Iwamoto^{1,2}

(1 IIS, Univ. Tokyo, 2 NanoQuine, Tokyo Univ.)

P-33

Characteristics of a Quantum Dot Infrared Photodetector on On-Axis Si (100) Substrate

H. Yoshikawa^{1,2}, J. -K. Kwoen¹, T. Doe^{1,2}, M. Izumi², S. Iwamoto¹, Y. Arakawa¹

(1 NanoQuine, Univ. Tokyo, 2 SHARP Corp.)

P-34

Investigation of Photoluminescence Intensity of GaInAs/InP Quantum-Wells Irradiated by Various Fast Atom Beam Sources

Y. Wang¹, T. Mitarai¹, T. Amemiya^{1,2}, N. Nishiyama^{1,2}, and S. Arai^{1,2}

(1 Dept. EEE, Tokyo Inst. Tech., 2 FIRST Lab, Tokyo Inst. Tech.)

P-35

MBE Growth of GaInAs Metamorphic Layers and 1.5- μ m-Band InAs/GaInAs Quantum Dots on GaAs Substrates

K. Watanabe¹, W. Zhan¹, M. Kakuda¹, J. -K. Kwoen¹, and Y. Arakawa¹

(1 NanoQuine, Univ. Tokyo)

P-36

Single Plasmon Generation in a Plasmonic Microring Resonator Embedding Self-Assembled Quantum Dots

A. Tamada¹, Y. Ota², K. Kurama¹, K. Watanabe², S. Iwamoto^{1,2}, and Y. Arakawa²

(1 IIS, Univ. Tokyo, 1 NanoQuine, Univ. Tokyo)